



Pin.No	Symbol	Description	Description	
1	VDD	Supply Voltage for Logic(+5.0V) .	1.DISPLAY TYPE	STN/NEGIVE/BLUE
2	VDD	Supply Voltage for Logic(5.0V) .	2.VIEWING DIRECTION	6 O'CLOCK .
3	CS2	Chip selection input with Pull-high resistor.	3.POLARIZER MODE	TRANSMISSIVE
4	CS1	Chip selection input with Pull-high resistor.	4.DRIVE METHOD	1/8 DUTY, 1/4 BIAS
5	DATA	Serial data input or output with Pull-high resistor.	5. OPERATING VOLTAGE	5.0V
6	CLK	WRITE clock input with Pull-high resistor.	6.OPERATING TEMP	-20° ~ 70°C.
7	A	Backlight anode (+5.0V).	7.STORAGE TEMP	-30° ~ 80°C.
8	Key1	Key out	8.CONNECTOR	PIN
9	Key2	Key out	9.COLOR OF BACKLIGHT	WHITE
10	Key3	Key out	10.DRIVE IC	HT1622+HT1622
11	Key4	Key out		
12	NC	---		
13	VSS	Logic VSS		
14	VSS	Logic VSS		

REVISION RECORD		DATE	SHENZHEN ET TECHNOLOGY CO.LTD.	
			Email: east@eastet.com	
A	FIRST ISSUE		TITLE: LCM OUTLINE DIMENSION	GEN.TOL.:±0.2
B			Model NO. ET-S07406	PRO.(3):
C			Part NO. SPEC-ET-S07406 -BCSWSW	VER.: A
D			DRAWN JLS DATE 2014.04.12	SHEET: 1
E			CHECKED	UNIT: mm
F			APPROVED	SCALE: 1:1